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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/849,122	22 05/04/2001		Donald E. Ackley	264/037	1952
34263	7590	05/04/2005		EXAMINER	
O'MELVEN			MARSCHEL, ARDIN H		
114 PACIFICA, SUITE 100 IRVINE, CA 92618				ART UNIT	PAPER NUMBER
				1631	

DATE MAILED: 05/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
		09/849,122	ACKLEY ET AL.				
	Office Action Summary	Examiner	Art Unit				
		Ardin Marschel	1631				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SH THE - External afternal	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. It is period for reply specified above is less than thirty (30) days, a reply or period for reply is specified above, the maximum statutory period were to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be timy within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).				
Status							
2a)	Responsive to communication(s) filed on <u>14 February 2005</u> . This action is FINAL . 2b) This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims		•				
5)⊠ 6)⊠ 7)□	Claim(s) 1-3,7-14,17 and 21-41 is/are pending 4a) Of the above claim(s) is/are withdraw Claim(s) 1-3,7-14 and 21-25 is/are allowed. Claim(s) 26-41 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	vn from consideration.					
Applicati	on Papers	•					
10)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Example 1.	epted or b) objected to by the Eddrawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).				
Priority u	ınder 35 U.S.C. § 119						
a)[Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureause the attached detailed Office action for a list of	s have been received. s have been received in Application rity documents have been receive u (PCT Rule 17.2(a)).	on No d in this National Stage				
2) D Notic 3) D Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summary Paper Notice of Informal Pa 6) Other:	(PTO-413) te. <i>S/I/O.S</i> atent Application (PTO-152)				

U.S. Patent and Trademark Office PTOL-326 (Rev. 1-04)

DETAILED ACTION

Applicants' arguments, filed 2/14/05, have been fully considered and they are deemed to be persuasive to overcome previous rejections of record. Rejections and/or objections not reiterated from previous office actions are hereby withdrawn.

Unfortunately, upon reconsideration, the following rejections and/or objections are newly applied. They constitute the complete set presently being applied to the instant application.

Due to the newly applied rejections summarized hereinbelow, the finality of the office action, mailed 11/2/04, is hereby withdrawn. The amendment, filed 2/14/05, has been entered.

PRIOR ART REJECTIONS

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 26-41 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zhao et al. (P/N 5,962,856); taken in view of Wainer et al. (P/N 3,609,359).

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Zhao et al. has been summarized in the previous office action, mailed 2/14/03, as describing an imaging array with row and column transistor selection etc. as shown in the Figures and summarized in the abstract. The "DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT" section starting in column 4 details the sensing electrodes, circuitry, etc. as instantly claimed. Figure 7 of Zhao et al. depicts a supply circuit for the supply as instantly claimed which is a first and second supply due to supplying electrical voltage to the multiplexer and scanning control electronic components depicted in Figure 1B of the reference which also shows row and column transistors with a return electrode depicted as the ground connection in said Figure 1B shown as a down arrowhead. The X-ray array of Zhao et al. is for receiving a conductive solution including charged biological materials which is described in column 1. lines 9-24, due to being utilized for medical imaging in a hospital of patients which are well known to contain charged biological materials which are also conductive if exposed to electricity. Zhao et al., however, does not describe a permeation layer on the X-ray array as required for instant claim 26 and those dependent therefrom.

Wainer et al. improves on X-ray imaging via the description of a layered structure for amplifying X-ray images fit onto an X-ray cassette as summarized in the abstract. Thus, Wainer et al. is motivated and suggested to be utilized in X-ray imaging as in Zhao et al. which utilizes X-ray imaging practice as summarized above. Wainer et al. also is directed to medical X-ray practice as summarized in column 1, lines 7-17. The layered structure of Wainer et al. which includes a permeation layer as claimed in instant claim 26, for example, is described in column 4, lines 21-56, wherein pores are

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set forth in the amplification layer of Wainer et al. which reasonably describe a permeation layer via porosity therein. Further porous or permeable layers are described in column 5, lines 30-73, in Wainer et al.

Thus, it would have been obvious to someone of ordinary skill in the art at the time of the instant invention to utilize the X-ray amplification invention of Wainer et al. for improved medical imaging which would also improve the practice of Zhao et al. regarding medical X-ray imaging with arrays utilized therein of transistors as summarized above to result in the practice of the above listed instant claims. The practice of field effect transistors in the circuitry of Zhao et al. as in instant claim 28 is described via the thin film transistor description in column 4, lines 50-57, wherein this transistor type utilizes a drain, source, and gate as are well known components described for field effect transistors.

Claims 1-3, 7-14, 17, and 21-25 are allowable.

Papers related to this application may be submitted to Technical Center 1600 by facsimile transmission. Papers should be faxed to Technical Center 1600 via the Central PTO Fax Center. The faxing of such papers must conform with the notices published in the Official Gazette, 1096 OG 30 (November 15, 1988), 1156 OG 61 (November 16, 1993), and 1157 OG 94 (December 28, 1993)(See 37 CFR § 1.6(d)). The Central PTO Fax Center number is (571) 273-8300.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ardin Marschel, Ph.D., AU 1631 Supervisory Patent Examiner, whose telephone number is (571) 272-0718. The examiner can normally be reached on Monday-Friday from 8 A.M. to 4 P.M.

Any inquiry of a general nature or relating to the status of this application should be directed to Legal Instrument Examiner, Tina Plunkett, whose telephone number is (571) 272-0549.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

May 1, 2005

ARDIN H. MARSCHEL PRIMARY EXAMINER